

Enhancement Mode N-Channel Power MOSFET

Features

- ◆ Low $R_{DS(on)}$ & FOM
- ◆ Extremely low switching loss
- ◆ Excellent stability and uniformity
- ◆ Easy to drive

Applications

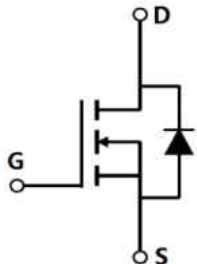

- ◆ Lighting
- ◆ Hard switching PWM
- ◆ Server power supply
- ◆ Charger

■ General Description

OSG65R290FTF uses advanced GreenMOS™ technology to provide low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. This device is suitable for active power factor correction and switching mode power supply applications.

◆ $V_{DS, min@Tjmax}$	700 V
◆ $I_D, pulse$	45 A
◆ $R_{DS(ON), max @ V_{GS}=10 V}$	290 mΩ
◆ Q_g	19.2 nC

■ Schematic and Package Information

Schematic Diagram 	Pin Assignment Top View  TO220F OSG65R290FTF
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■ Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	650	V
Gate source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	15	A
Continuous drain current ¹⁾ , $T_C=100^\circ\text{C}$		9.5	
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, pulse}$	45	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	15	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S, pulse}$	45	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	31	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	212	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\dots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\dots 480\text{ V}$, $I_{SD}\leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^\circ\text{C}$

■ Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	4	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62.5	$^{\circ}\text{C}/\text{W}$

■ Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}$
		700				$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}, T_j=150^{\circ}\text{C}$
Gate threshold voltage	$V_{GS(th)}$	3.0		4.0	V	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(on)}$		0.26	0.29	Ω	$V_{GS}=10\text{ V}, I_D=5\text{ A}$
			0.71			$V_{GS}=10\text{ V}, I_D=5\text{ A}, T_j=150^{\circ}\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=650\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	R_G		30		Ω	$f=1\text{ MHz}$, Open drain

■ Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		875		pF	$V_{GS}=0\text{ V}, V_{DS}=50\text{ V}, f=100\text{ KHz}$
Output capacitance	C_{oss}		64		pF	
Reverse transfer capacitance	C_{rss}		2.9		pF	
Turn-on delay time	$t_{d(on)}$		30		ns	$V_{GS}=10\text{ V}, V_{DS}=400\text{ V}, R_G=2\ \Omega, I_D=8\text{ A}$
Rise time	t_r		24.5		ns	
Turn-off delay time	$t_{d(off)}$		72		ns	
Fall time	t_f		19		ns	

■ Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		19.2		nC	$I_D=8\text{ A}$, $V_{DS}=400\text{ V}$, $V_{GS}=10\text{ V}$
Gate-source charge	Q_{gs}		6.3		nC	
Gate-drain charge	Q_{gd}		3.2		nC	
Gate plateau voltage	V_{plateau}		5.7		V	

■ Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	$I_S=15\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		252		ns	$V_R=400\text{ V}$, $I_S=8\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		2.6		μC	
Peak reverse recovery current	I_{rrm}		20		A	

■ Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25\text{ }^\circ\text{C}$.
- 5) $V_{DD}=100\text{ V}$, $R_G=50\text{ }\Omega$, $L=79.9\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

■ **Electrical Characteristics Diagrams**

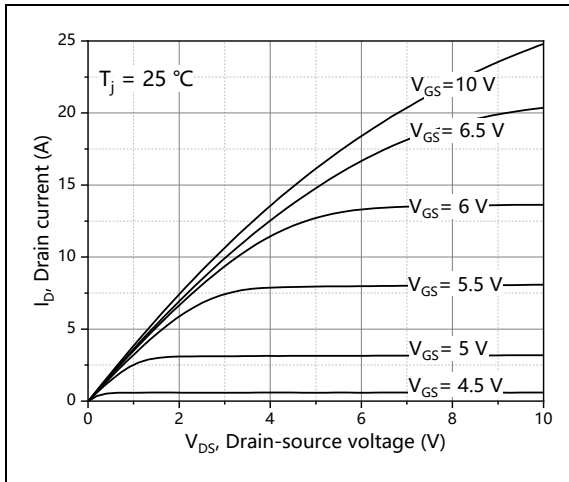


Figure 1, Typ. output characteristics

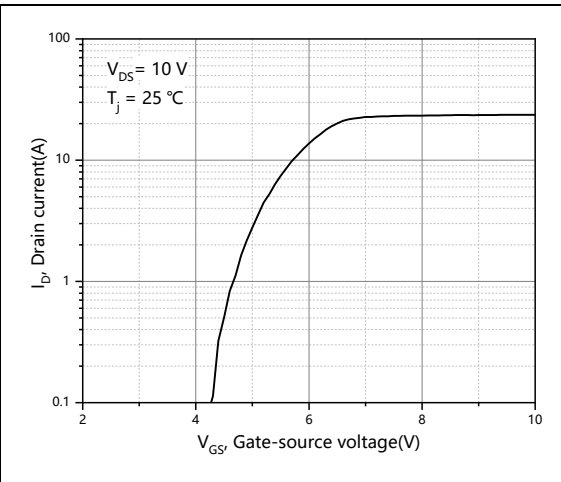


Figure 2, Typ. transfer characteristics

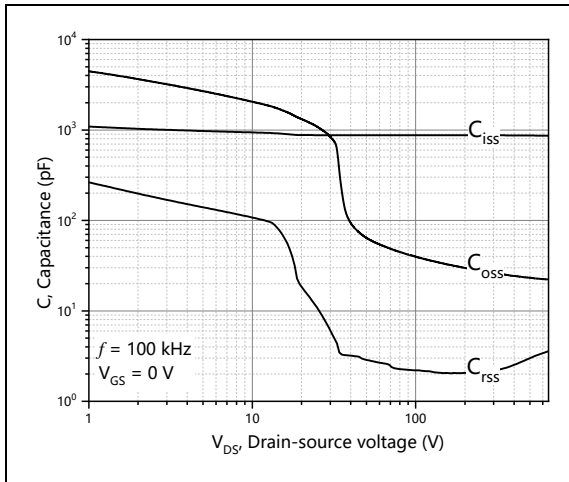


Figure 3, Typ. capacitances

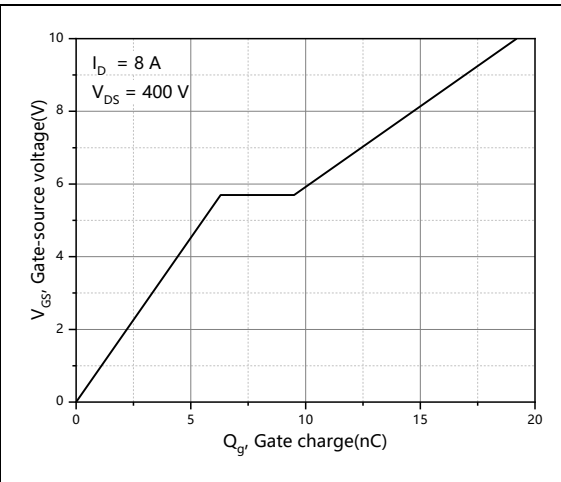


Figure 4, Typ. gate charge

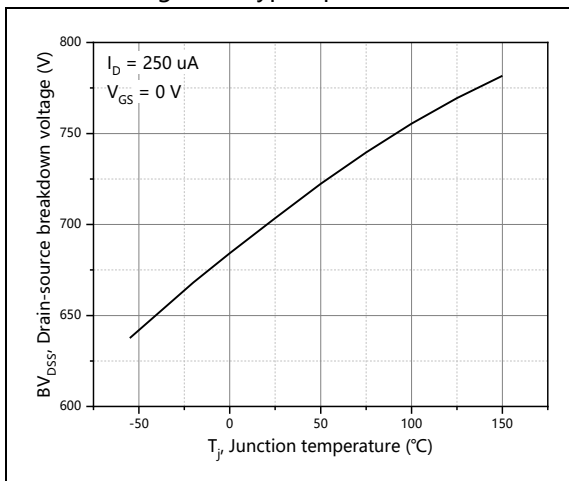


Figure 5, Drain-source breakdown voltage

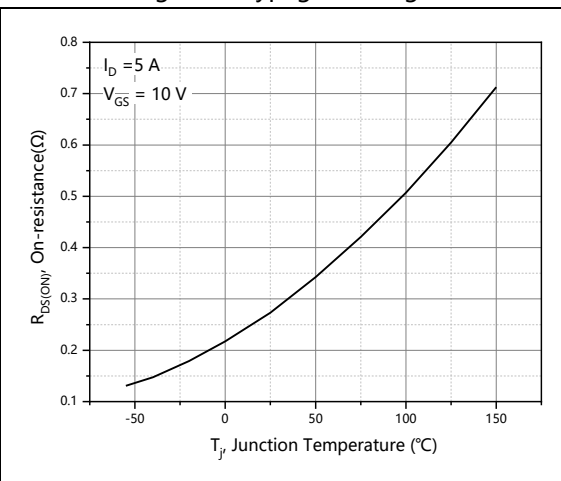


Figure 6, Drain-source on-state resistance

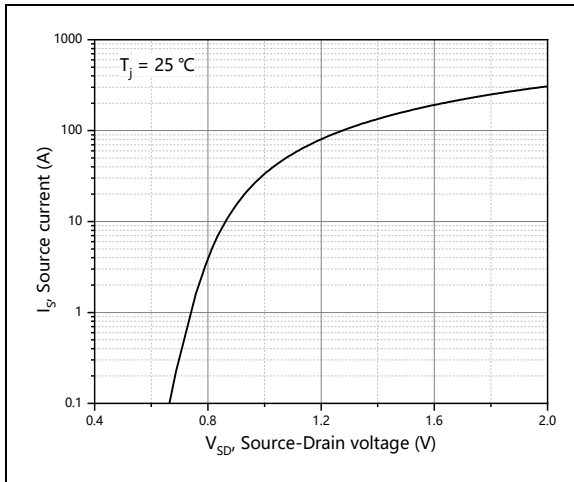


Figure 7, Forward characteristic of body diode

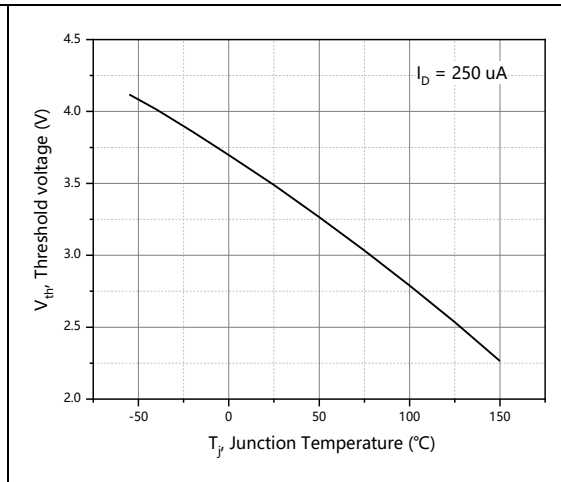


Figure 8, Threshold voltage

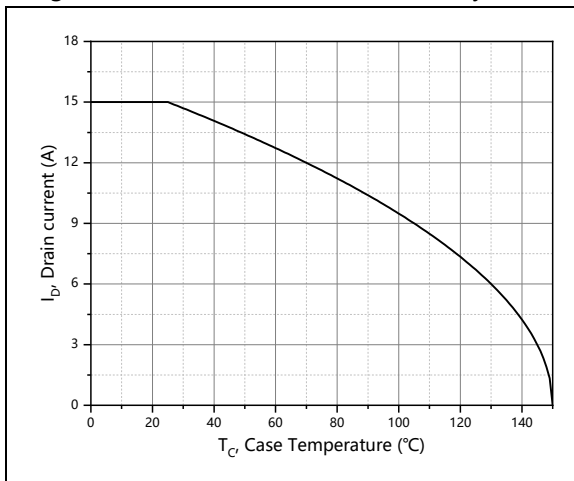


Figure 9, Drain current

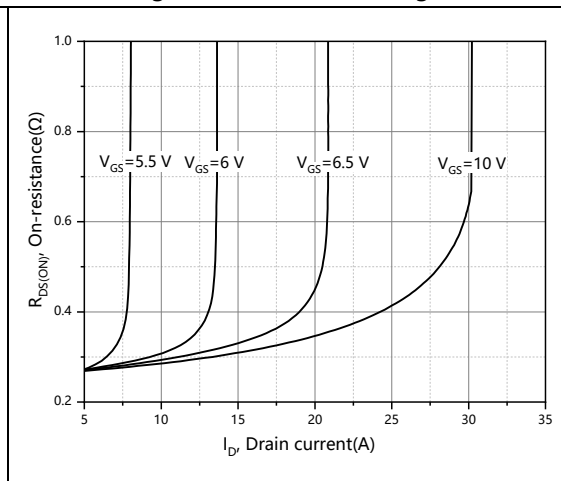


Figure 10, Drain-source on-state resistance

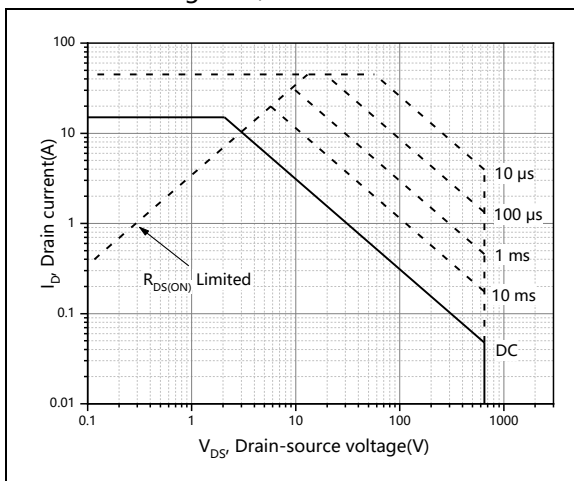


Figure 11, Safe operation area for $T_C = 25\text{ °C}$

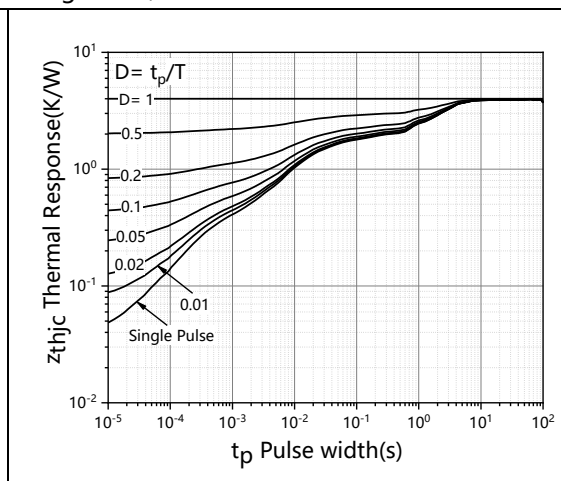


Figure 12, Max. transient thermal impedance

■ Test circuits and waveforms

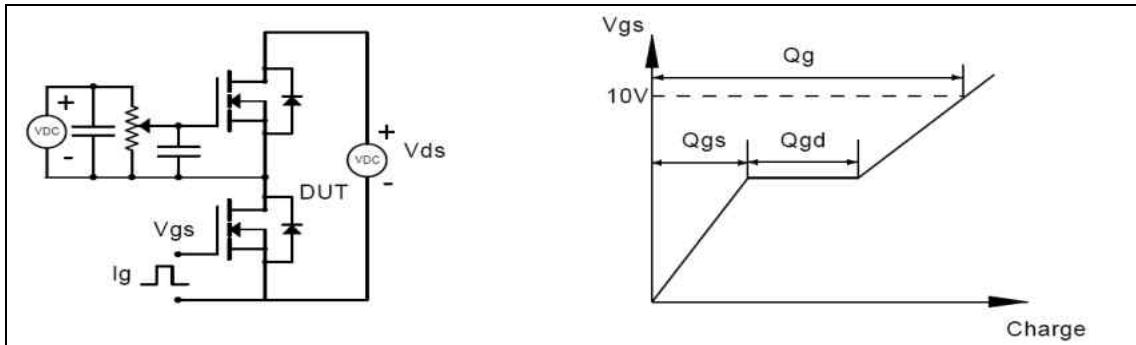


Figure 1, Gate charge test circuit & waveform

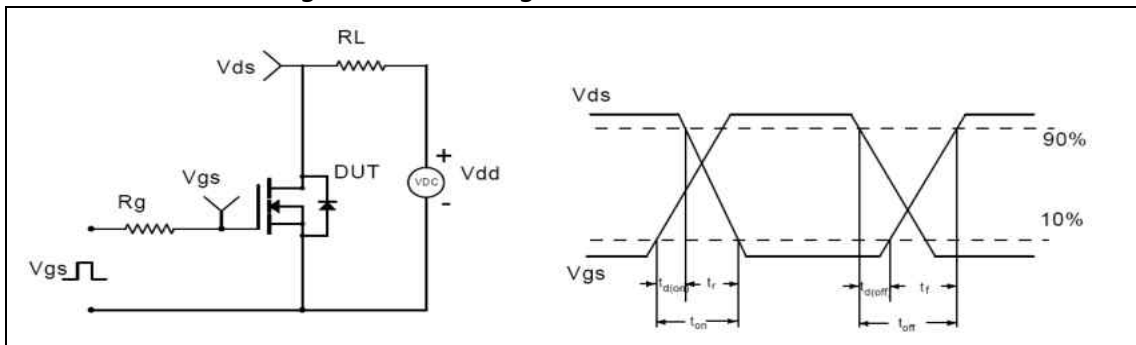


Figure 2, Switching time test circuit & waveforms

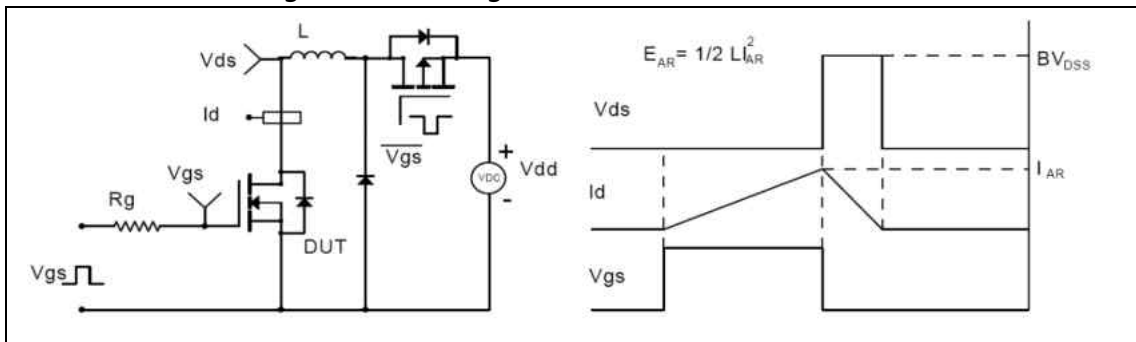


Figure 3, Unclamped inductive switching (UIS) test circuit & waveforms

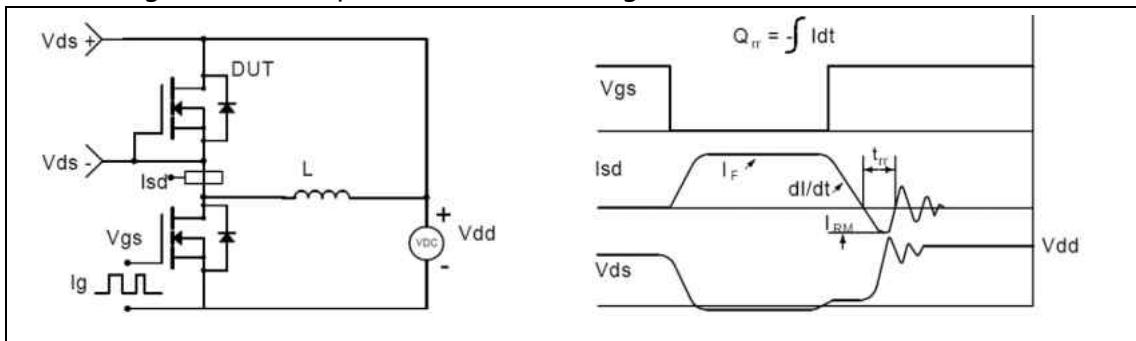
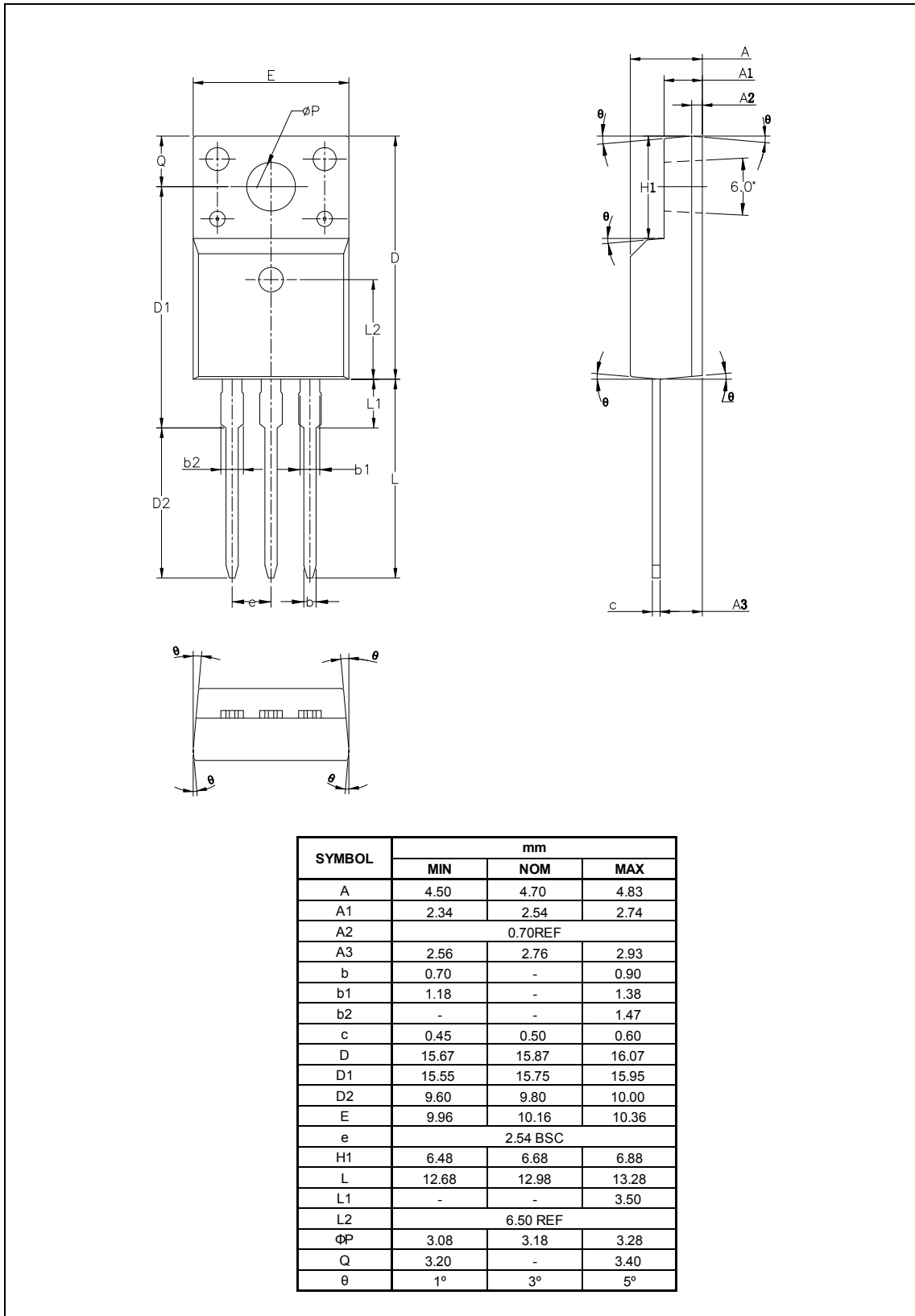


Figure 4, Diode reverse recovery test circuit & waveforms

■ **Package Information**

Figure1, TO220F package outline dimension



■ Ordering Information

Package	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Box/Carton Box	Units/Carton Box
TO220F	50	20	1000	5	5000

■ Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R290FTF	TO220F	yes	yes	yes