



ORIENTAL
SEMICONDUCTOR

GreenMOS™

OSG65R360xEF_Datasheet

Green
PRODUCT

RoHS
compliant

Enhancement Mode N-Channel Power MOSFET

Features

- ◆ Low $R_{DS(on)}$ & FOM
- ◆ Extremely low switching loss
- ◆ Excellent stability and uniformity
- ◆ Easy to drive
- ◆ EMI and performance balanced

Applications

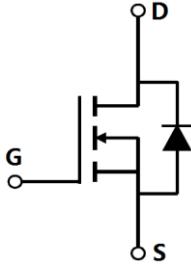
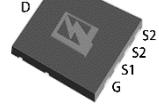
- ◆ Lighting
- ◆ Hard switching PWM
- ◆ Server power supply
- ◆ Charger

■ General Description

OSG65R360xEF use advanced GreenMOS™ technology to provide low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. This device is suitable for active power factor correction and switching mode power supply applications.

◆ $V_{DS, min@Tjmax}$	700 V
◆ $I_D, pulse$	36 A
◆ $R_{DS(ON)}, \text{max } @ VGS=10 \text{ V}$	360 mΩ
◆ Q_g	15.2 nC

■ Schematic and Package Information

Schematic Diagram	Pin Assignment Top View		
		TO252	
	OSG65R360DEF	TO220	OSG65R360PEF
		PDFN8x8	OSG65R360JEF

■ Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	650	V
Gate source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_c=25^\circ\text{C}$	I_D	12	A
Continuous drain current ¹⁾ , $T_c=100^\circ\text{C}$		7.6	
Pulsed drain current ²⁾ , $T_c=25^\circ\text{C}$	$I_{D, \text{pulse}}$	36	A
Power dissipation ³⁾ , $T_c=25^\circ\text{C}$	P_D	83	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	200	mJ
MOSFET dV/dt ruggedness, $V_{DS}=0\ldots 480 \text{ V}$	dV/dt	50	V/ns
Reverse diode dV/dt, $V_{DS}=0\ldots 480 \text{ V}, I_{SD} \leq I_D$	dV/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

■ Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R_{\thetaJC}	1.5	°C/W
Thermal resistance, junction-ambient ⁴⁾	R_{\thetaJA}	62	°C/W

■ Electrical Characteristics at $T_j=25$ °C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	$V_{GS}=0$ V, $I_D=250$ μA
		700	770			$V_{GS}=0$ V, $I_D=250$ μA, $T_j=150$ °C
Gate threshold voltage	$V_{GS(th)}$	2.9		3.9	V	$V_{DS}=V_{GS}$, $I_D=250$ μA
Drain-source on-state resistance	$R_{DS(ON)}$		0.30	0.36	Ω	$V_{GS}=10$ V, $I_D=3$ A
			0.76			$V_{GS}=10$ V, $I_D=3$ A, $T_j=150$ °C
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30$ V
				-100		$V_{GS}=-30$ V
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=650$ V, $V_{GS}=0$ V

■ Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		815.1		pF	$V_{GS}=0$ V, $V_{DS}=50$ V, $f=1$ MHz
Output capacitance	C_{oss}		59.6		pF	
Reverse transfer capacitance	C_{rss}		2.9		pF	
Turn-on delay time	$t_{d(on)}$		30.8		ns	$V_{GS}=10$ V, $V_{DS}=400$ V, $R_G=2$ Ω, $I_D=6$ A
Rise time	t_r		18.6		ns	
Turn-off delay time	$t_{d(off)}$		71.1		ns	
Fall time	t_f		14.0		ns	

■ Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		15.2		nC	$I_D=6\text{ A}$, $V_{DS}=400\text{ V}$, $V_{GS}=10\text{ V}$
Gate-source charge	Q_{gs}		3.3		nC	
Gate-drain charge	Q_{gd}		5.9		nC	
Gate plateau voltage	$V_{plateau}$		6.3		V	

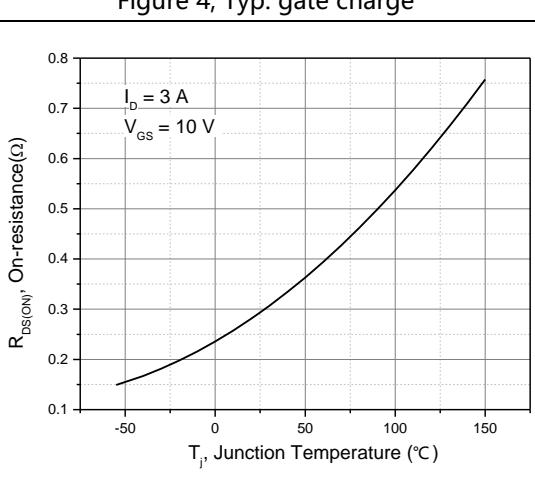
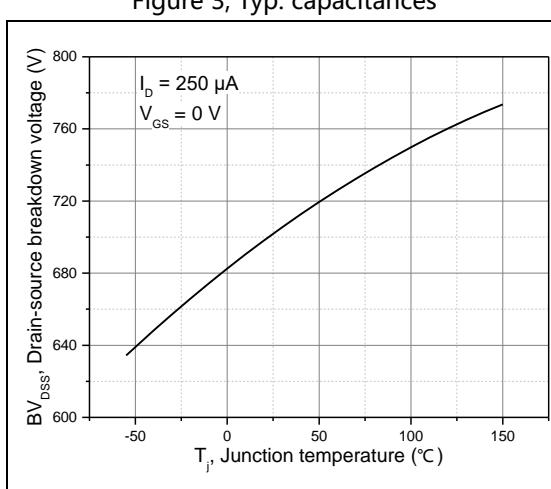
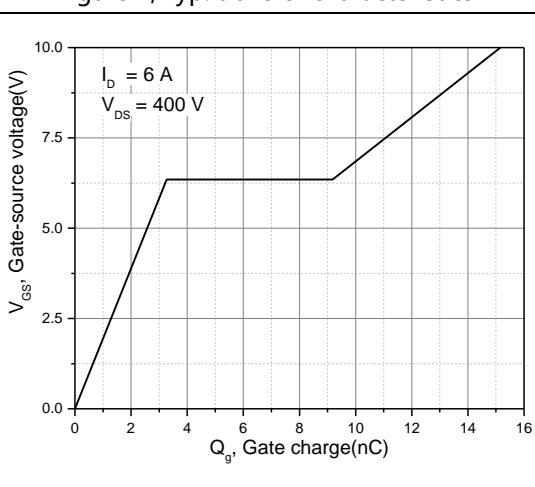
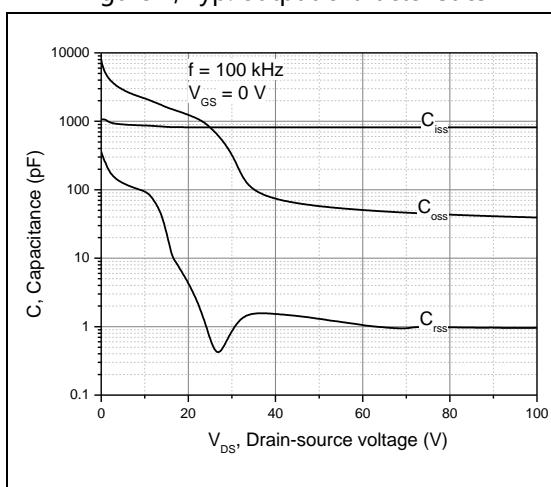
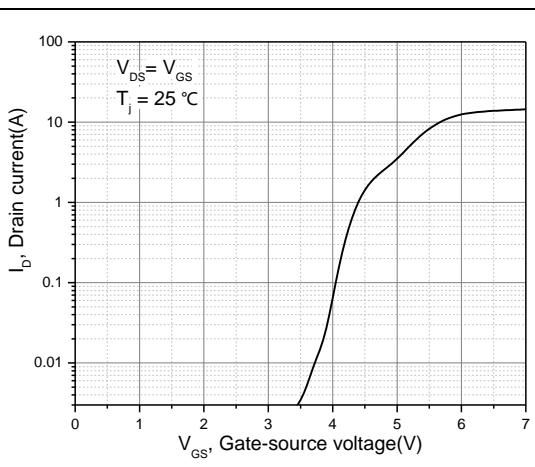
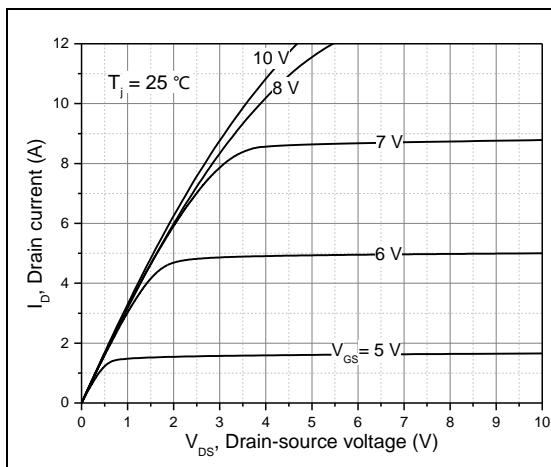
■ Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward current	I_S			12	A	$V_{GS} < V_{th}$
Pulsed source current	I_{SP}			36		
Diode forward voltage	V_{SD}			1.3	V	$I_S=12\text{ A}, V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		232.0		ns	$V_R=400\text{ V}, I_S=6\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		2.1		μC	
Peak reverse recovery current	I_{rrm}		19.5		A	

■ Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25\text{ }^\circ\text{C}$.
- 5) $V_{DD}=100\text{ V}$, $R_G=50\text{ }\Omega$, $L=60\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

■ Electrical Characteristics Diagrams



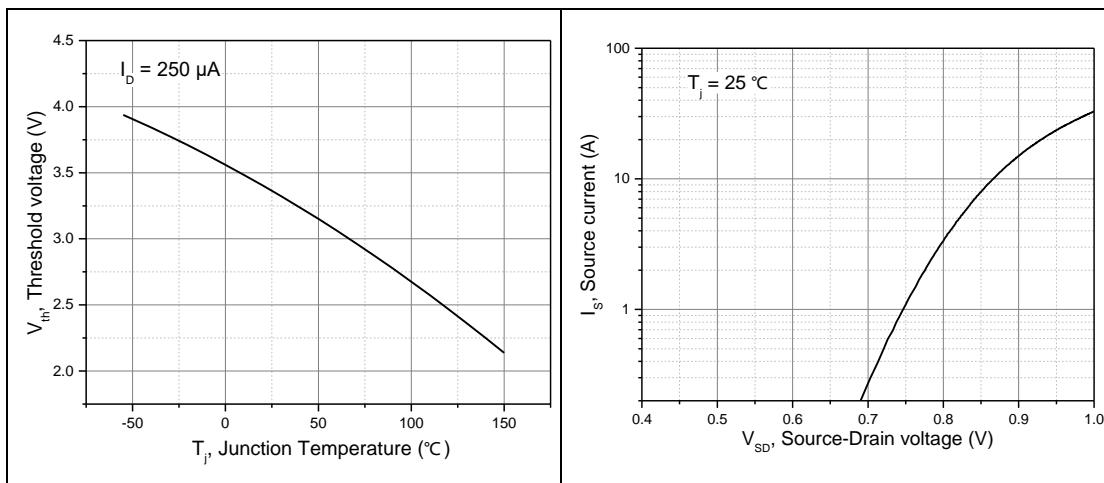


Figure 7, Threshold voltage

Figure 8, Forward characteristic of body diode

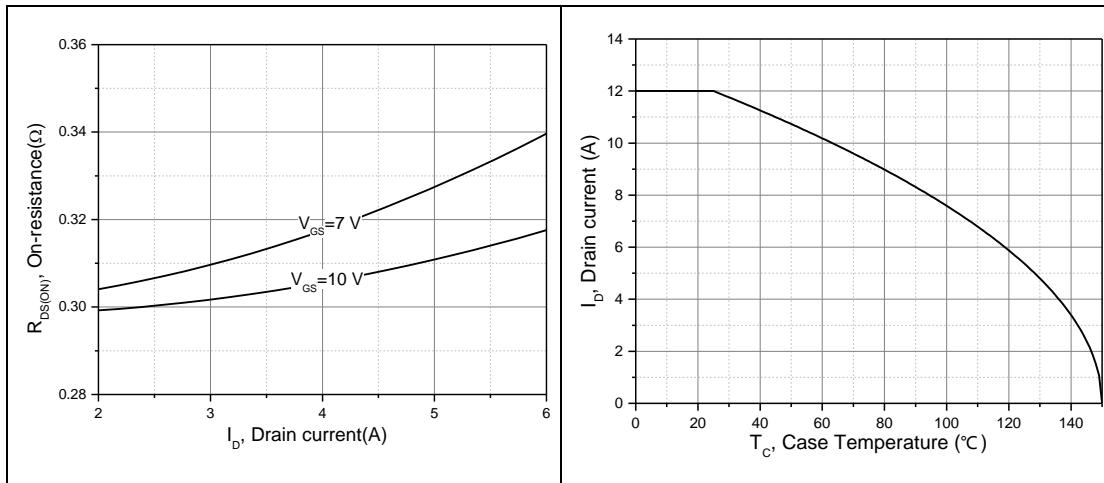
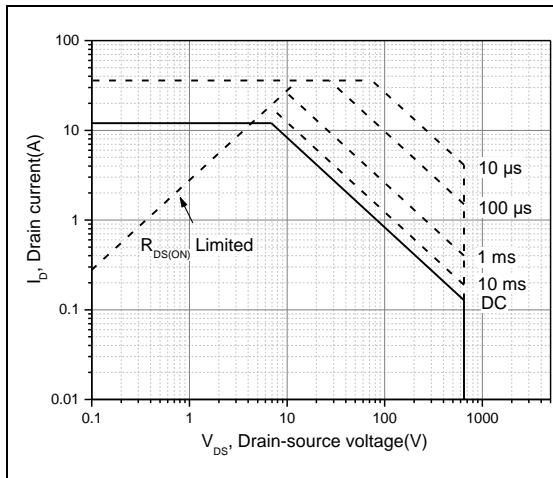


Figure 9, Drain-source on-state resistance

Figure 10, Drain current


 Figure 11, Safe operation area for $T_c=25\text{ }^{\circ}\text{C}$

■ Test circuits and waveforms

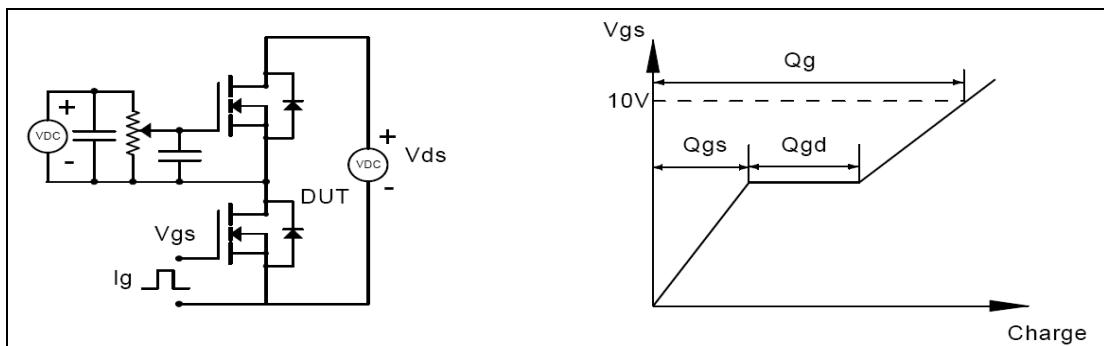


Figure 1, Gate charge test circuit & waveform

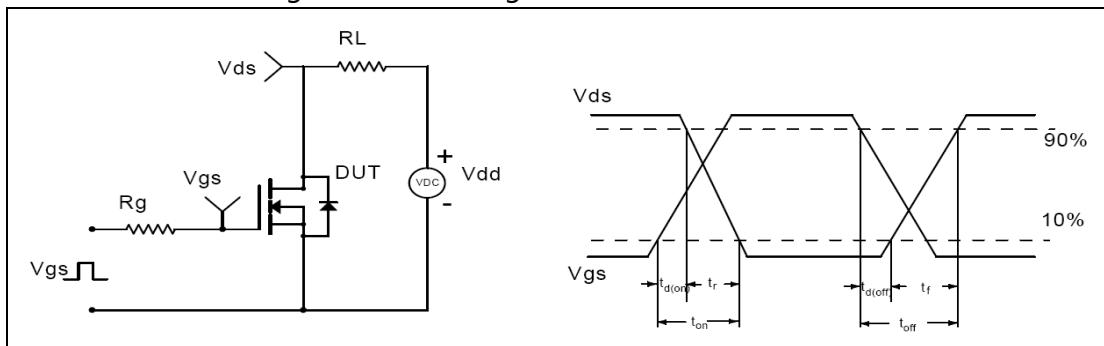


Figure 2, Switching time test circuit & waveforms

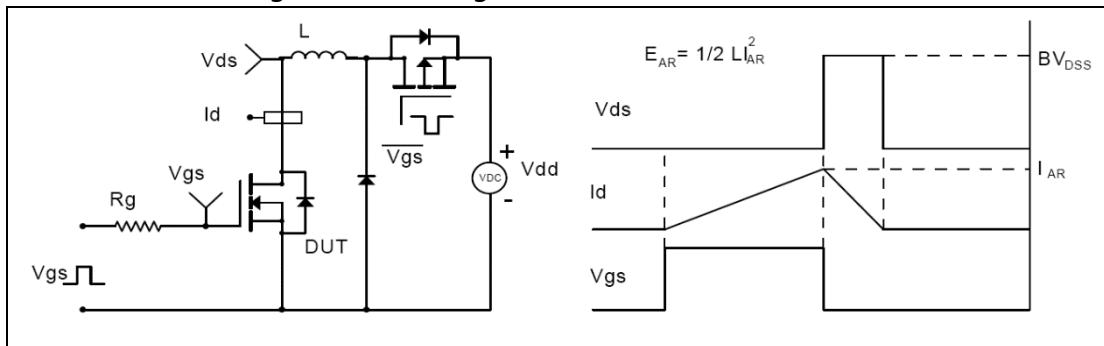


Figure 3, Unclamped inductive switching (UIS) test circuit & waveforms

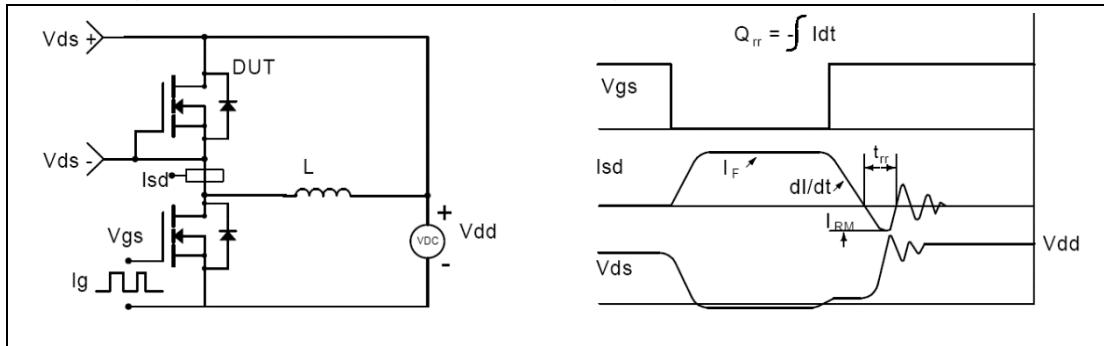
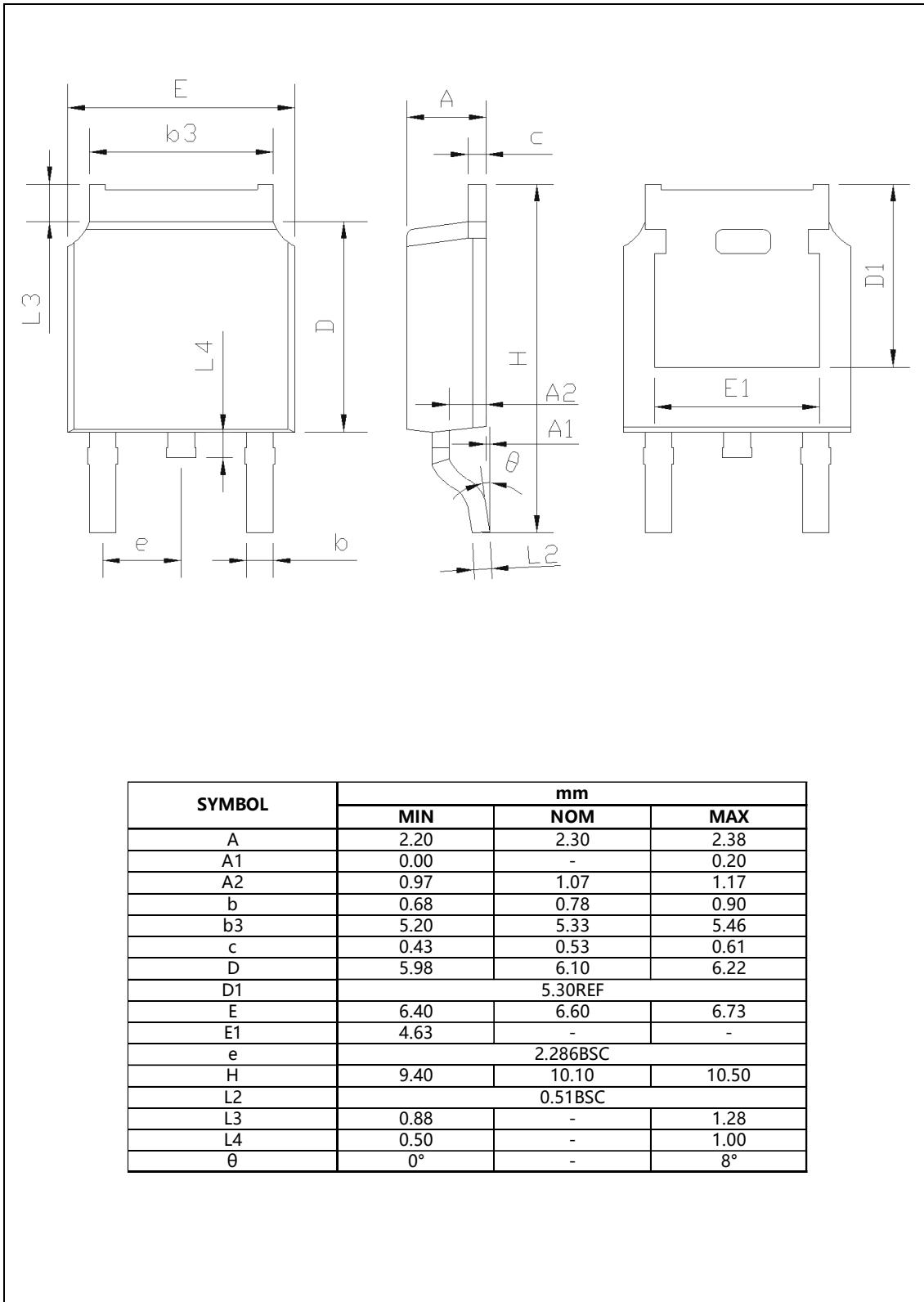


Figure 4, Diode reverse recovery test circuit & waveforms

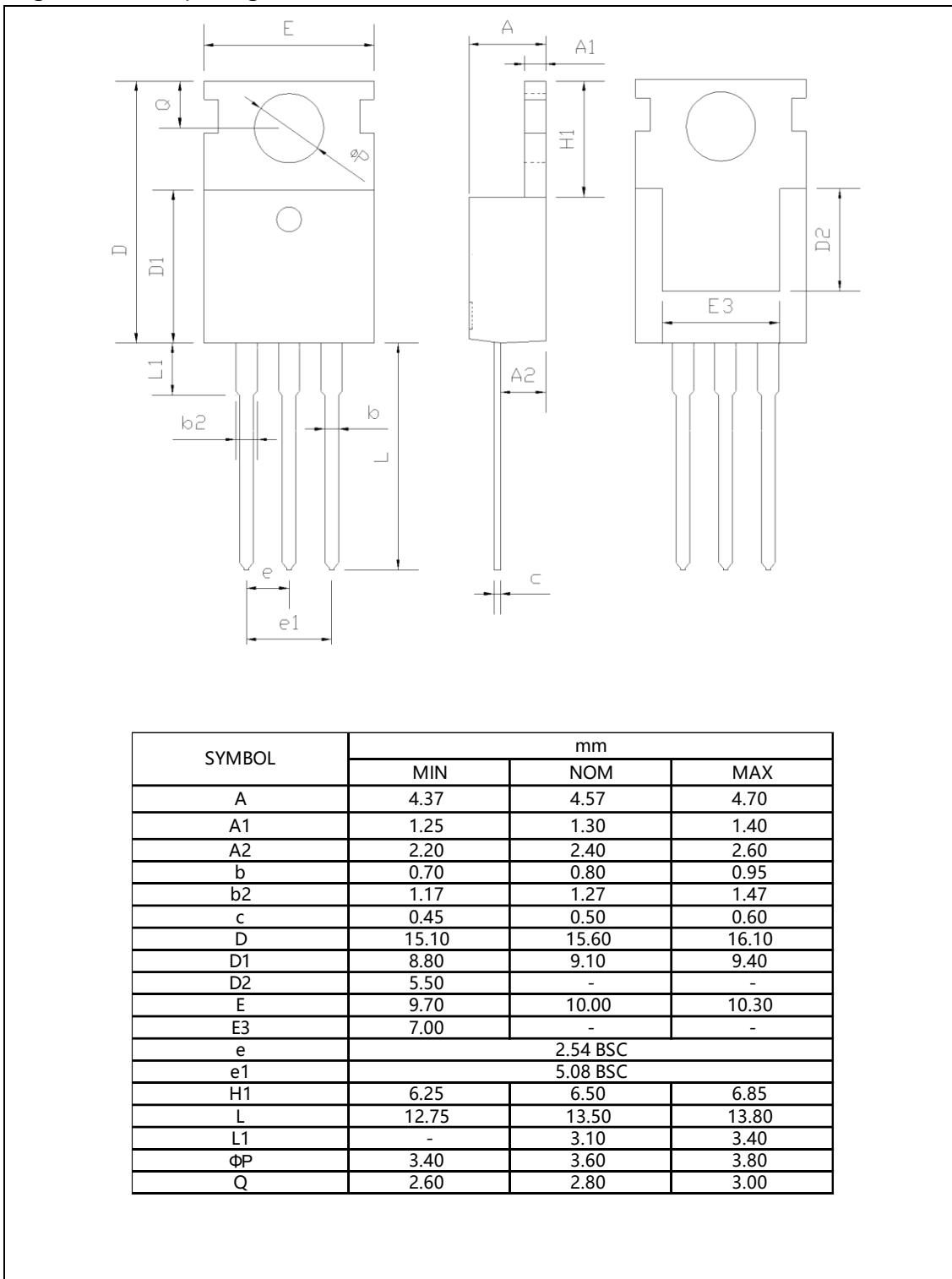
■ Package Information

Figure1, TO252 package outline dimension



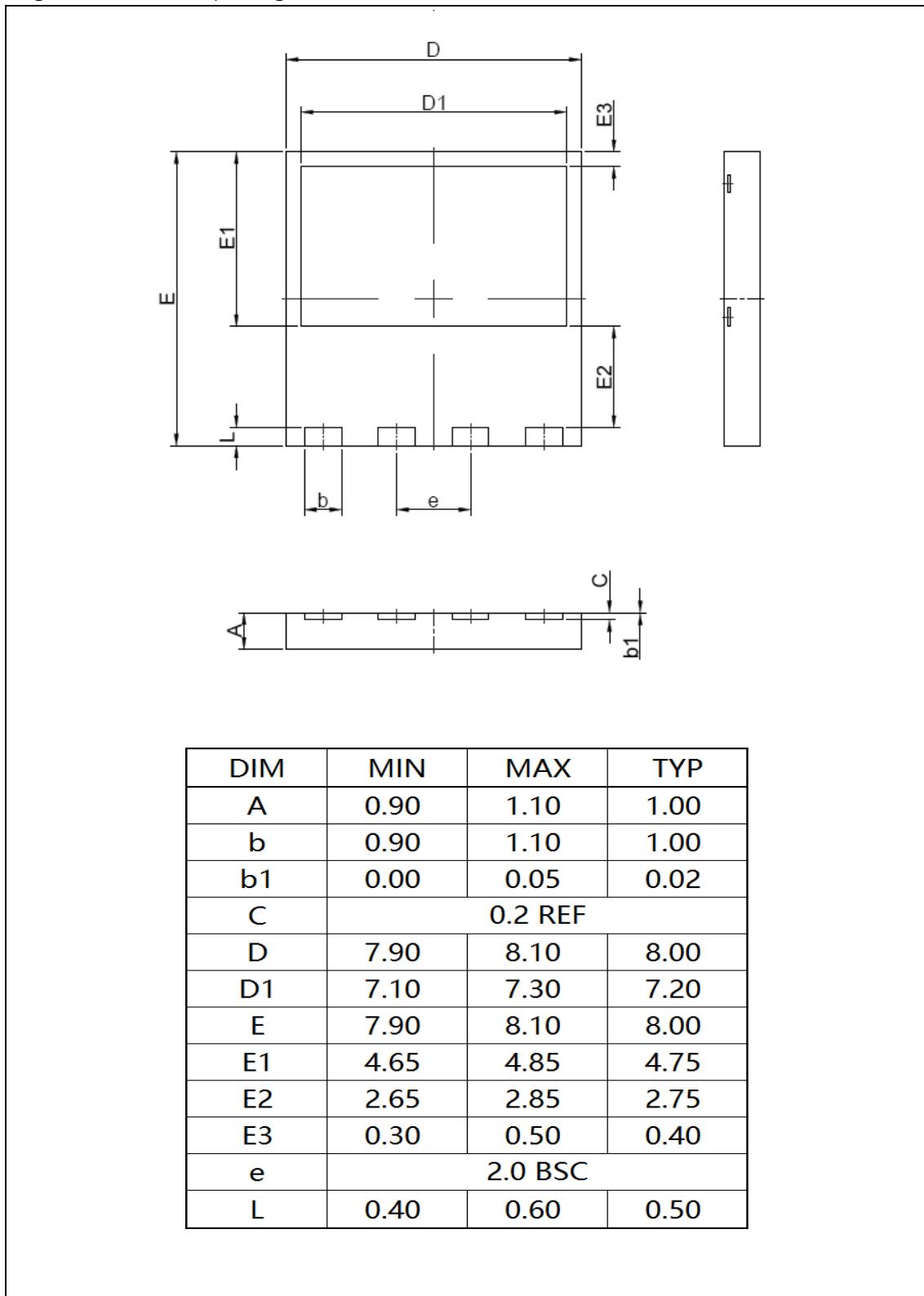
■ Package Information

Figure2, TO220 package outline dimension



■ Package Information

Figure3, PDFN8×8 package outline dimension



■ Ordering Information

Package	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Box/Carton Box	Units/Carton Box
TO252	2500	2	5000	5	25000
PDFN8×8	2500	1	2500	10	25000

Package	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Box/Carton Box	Units/Carton Box
TO220	50	20	1000	6	6000

■ Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R360DEF	TO252	yes	yes	yes
OSG65R360PEF	TO220	yes	yes	yes
OSG65R360JEF	PDFN8×8	yes	yes	yes